

METHODS AND APPARATUS FOR GENERATING PROGRAMMABLE DEVICE LAYOUT INFORMATION

Abstract of the Disclosure

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Methods and apparatus are provided for implementing a programmable device on a printed circuit board. Input data to implement a processor core and components on a programmable chip is received. The processor core and components can be customized based on particular user needs. The input data is used to generate the 10 programmable chip along with information allowing efficient and effective interconnectivity of the programmable chip with external devices.